



TITLE: NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM  
INVENTORS NAME: Martin M. Deneroff et al.  
SERIAL NO.: 09/408,972

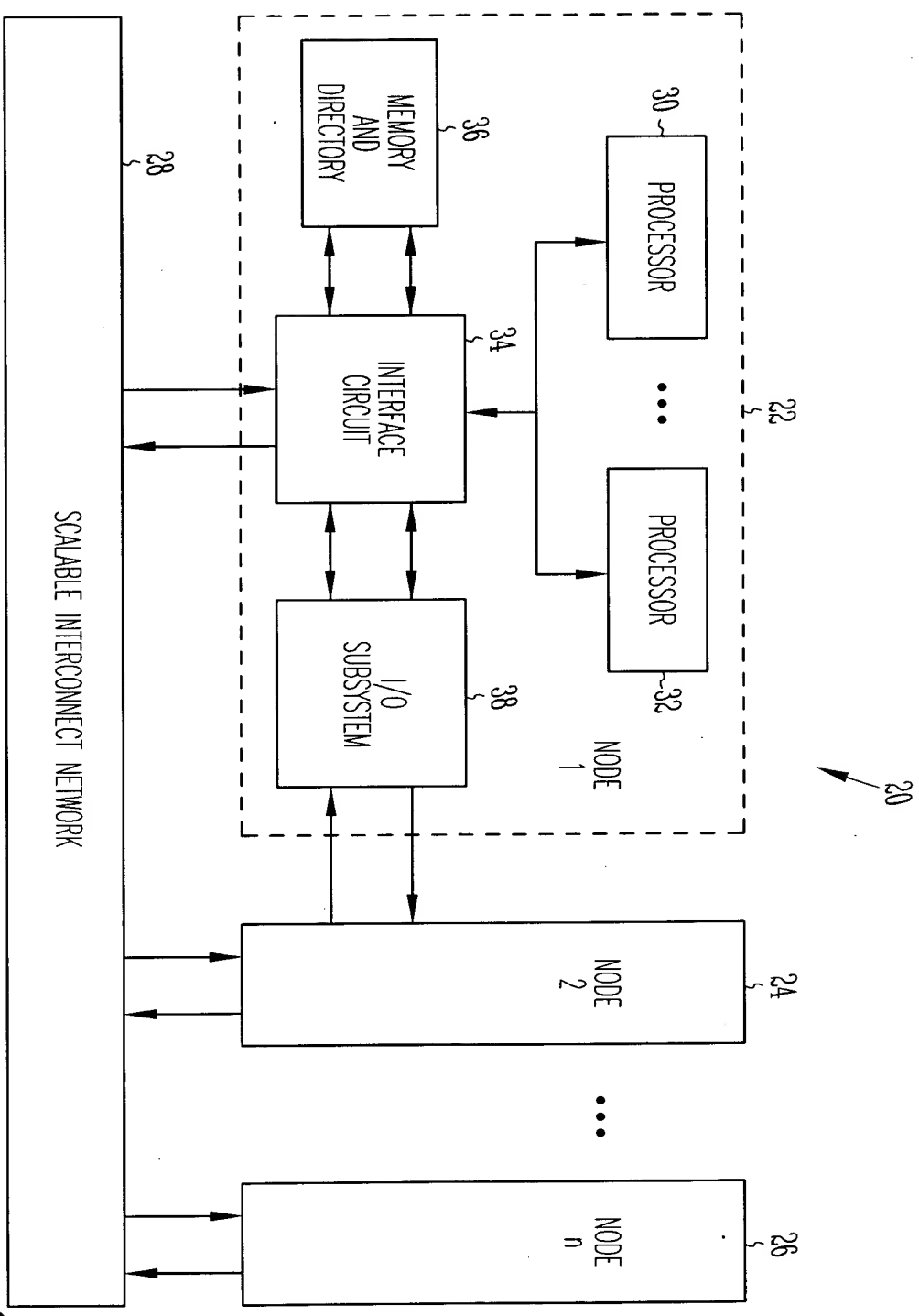


Fig. 1

RECEIVED  
SEP 09 2002  
Technology Center 2100

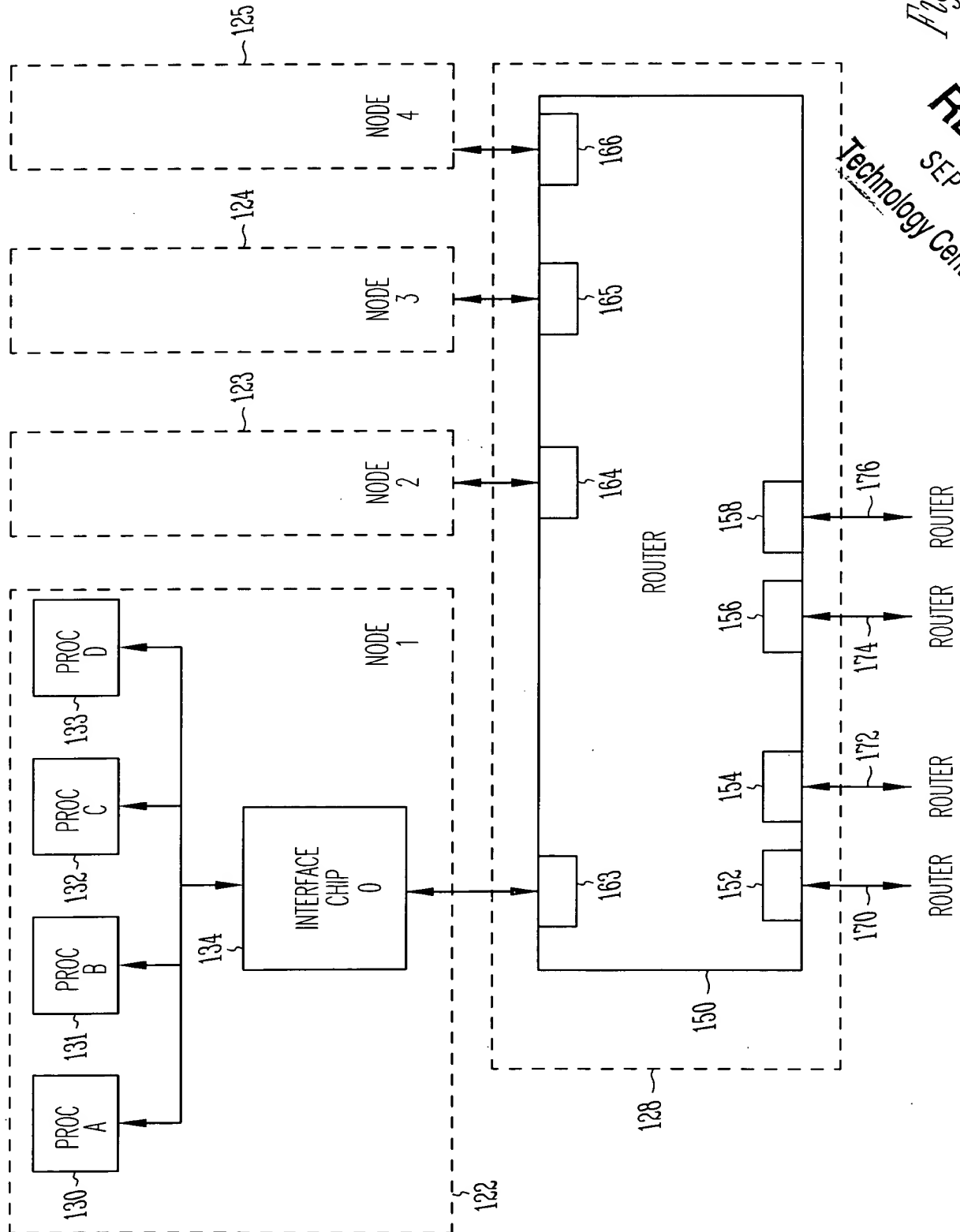


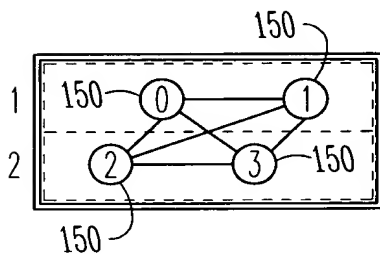
Fig. 2

RECEIVED  
SEP 09 2002  
Technology Center 2100



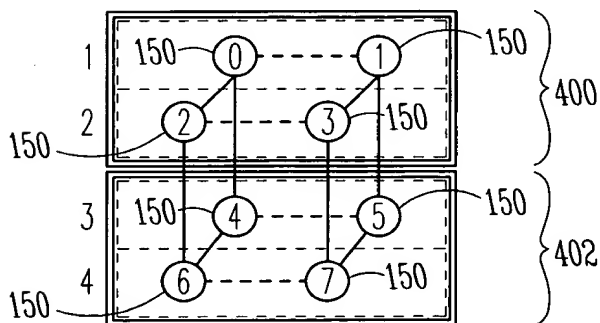
TITLE: NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM  
INVENTORS NAME: Martin M. Deneroff et al.  
SERIAL NO.: 09/408,972

3/15



*Fig. 3*

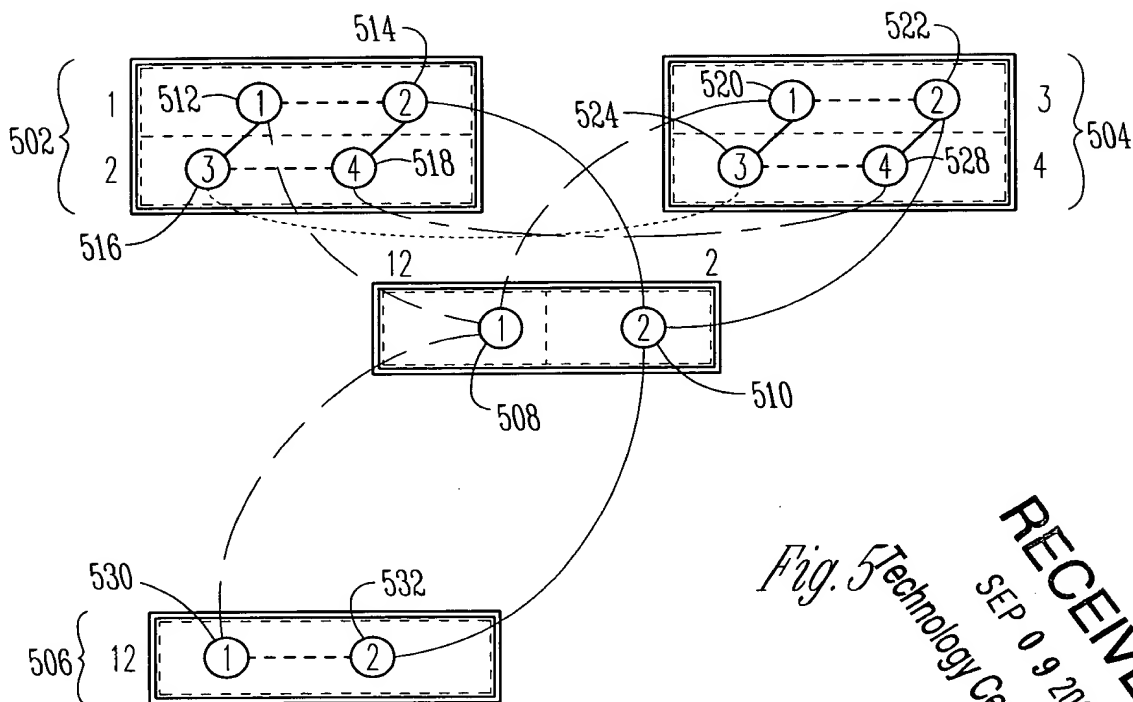
RECEIVED  
SEP 09 2002  
Technology Center 2100



*Fig. 4*



4/15



RECEIVED  
SEP 09 2002  
Technology Center 2100  
Fig. 5

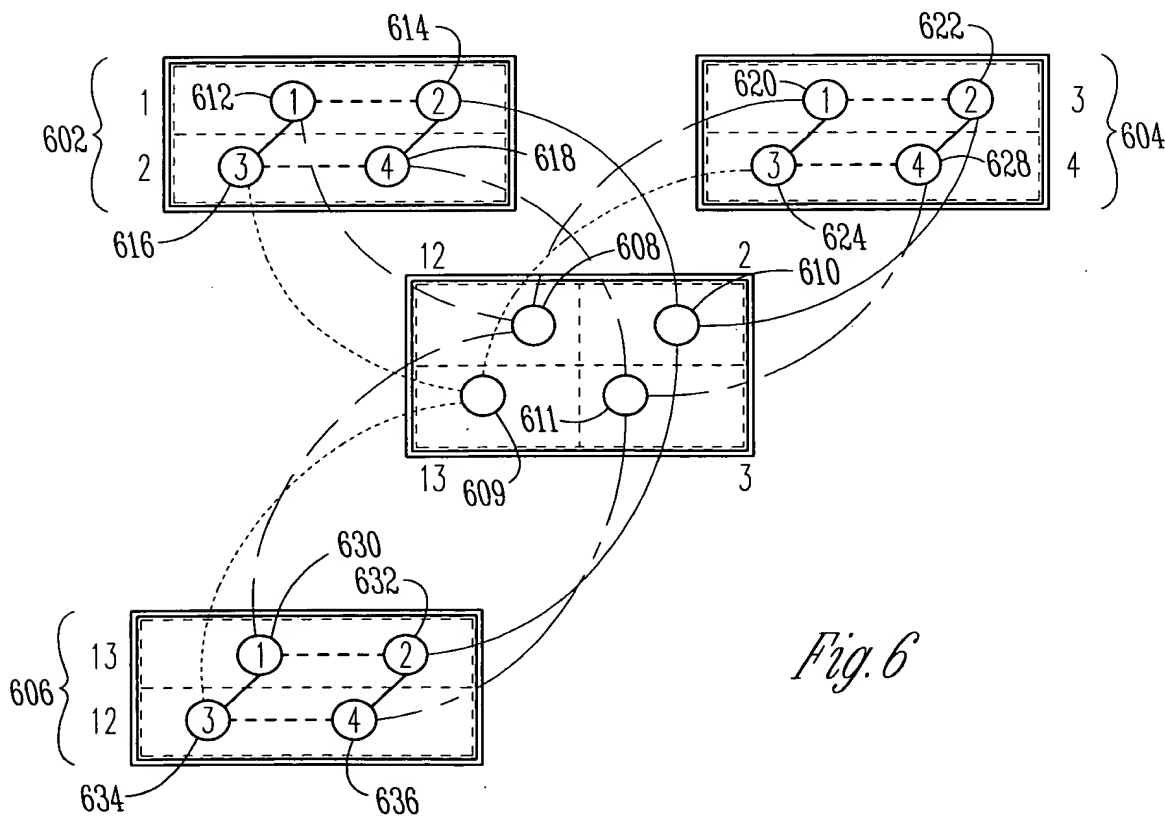


Fig. 6

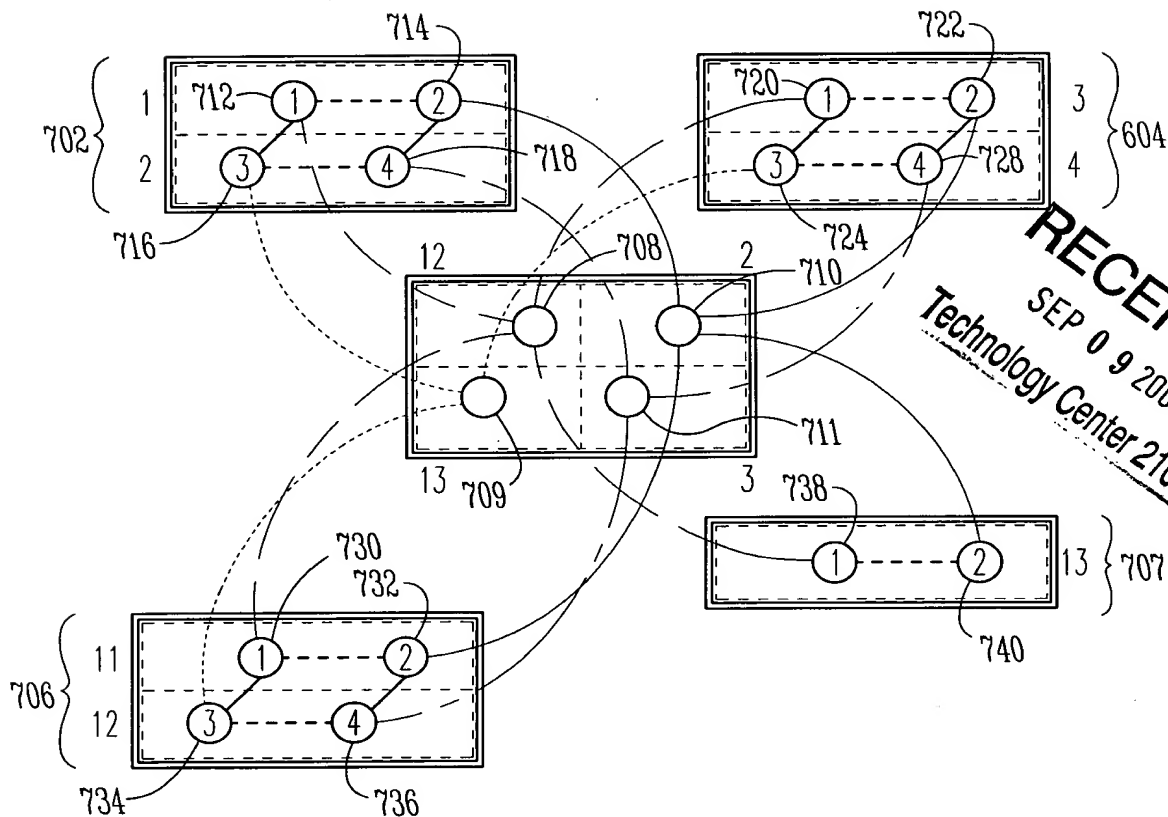
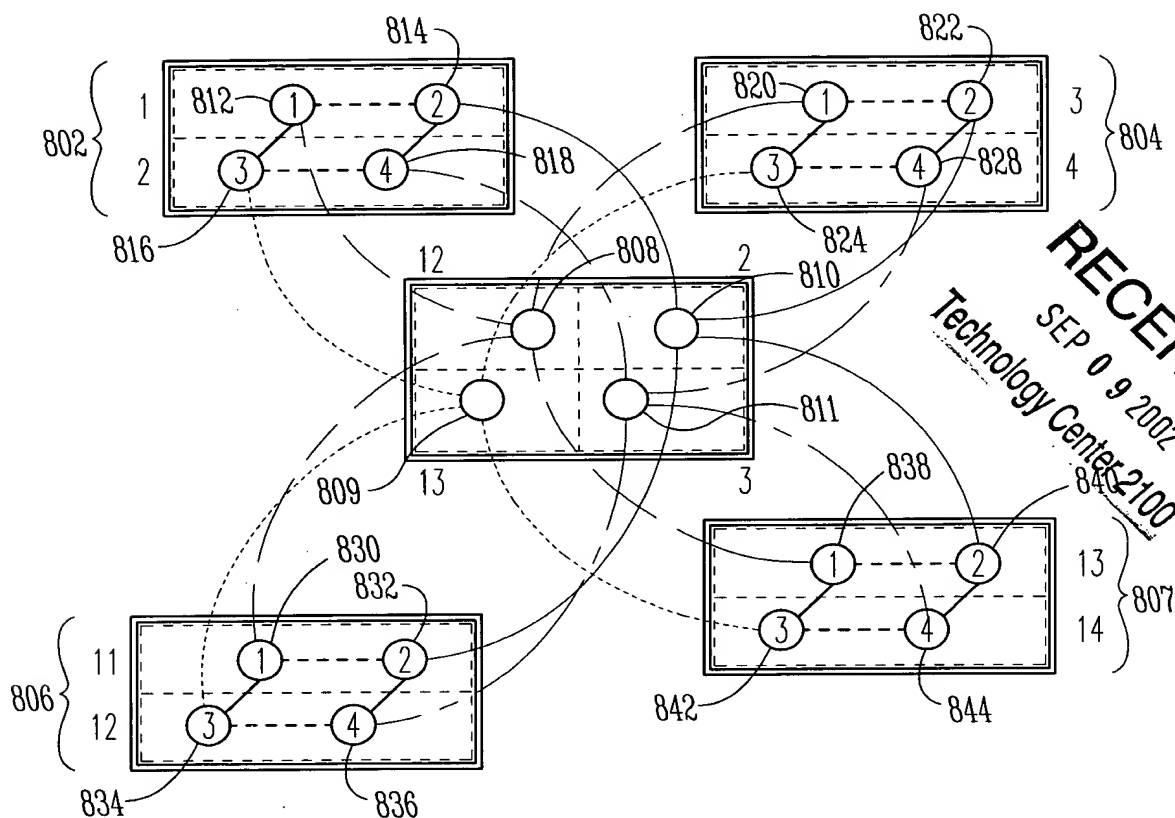
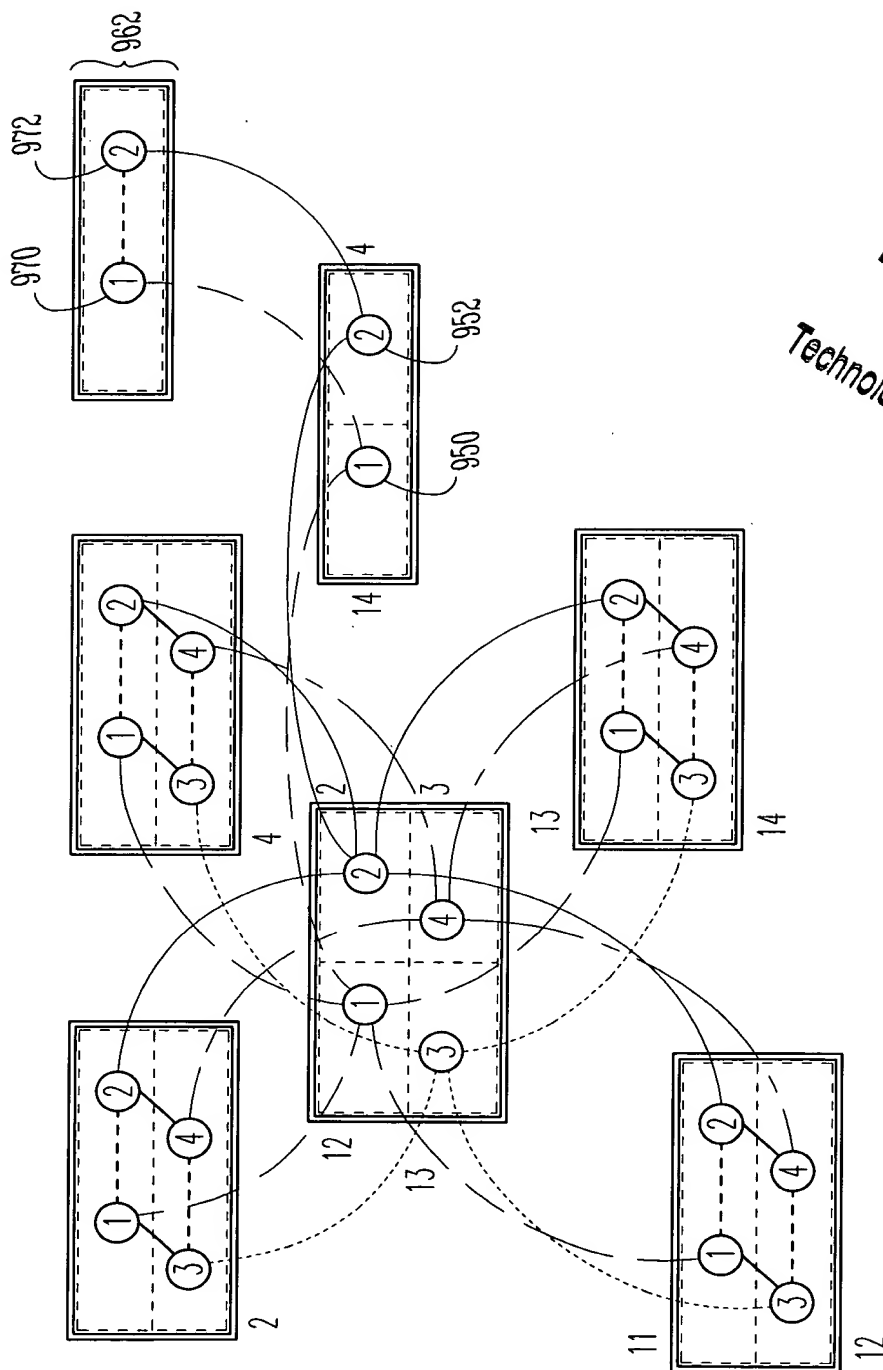


Fig. 7



RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 8



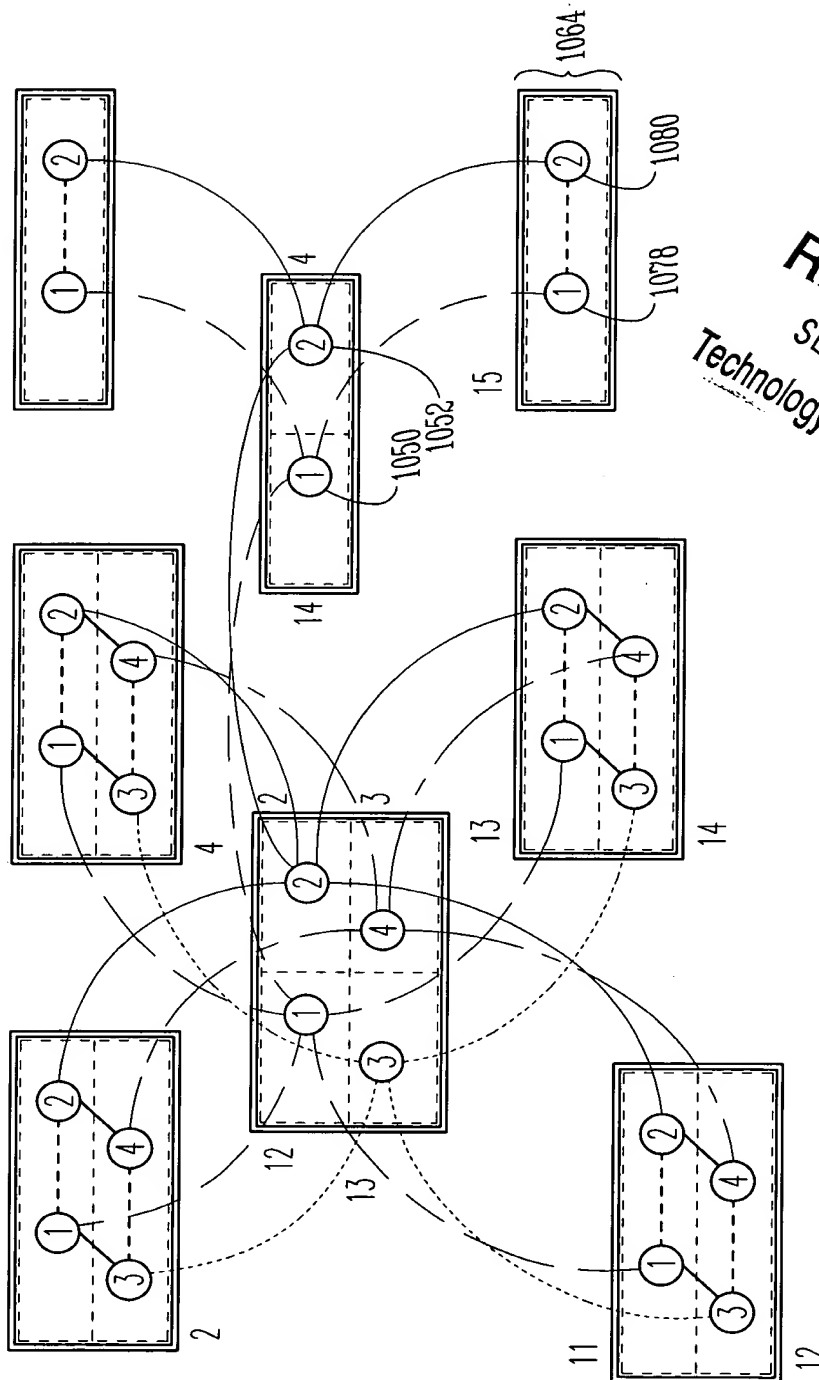
RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 9



TITLE: NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM  
INVENTORS NAME: Martin M. Deneroff et al.  
SERIAL NO.: 09/408,972

8/15



RECEIVED  
SEP 09 2002  
Technology Center 2100

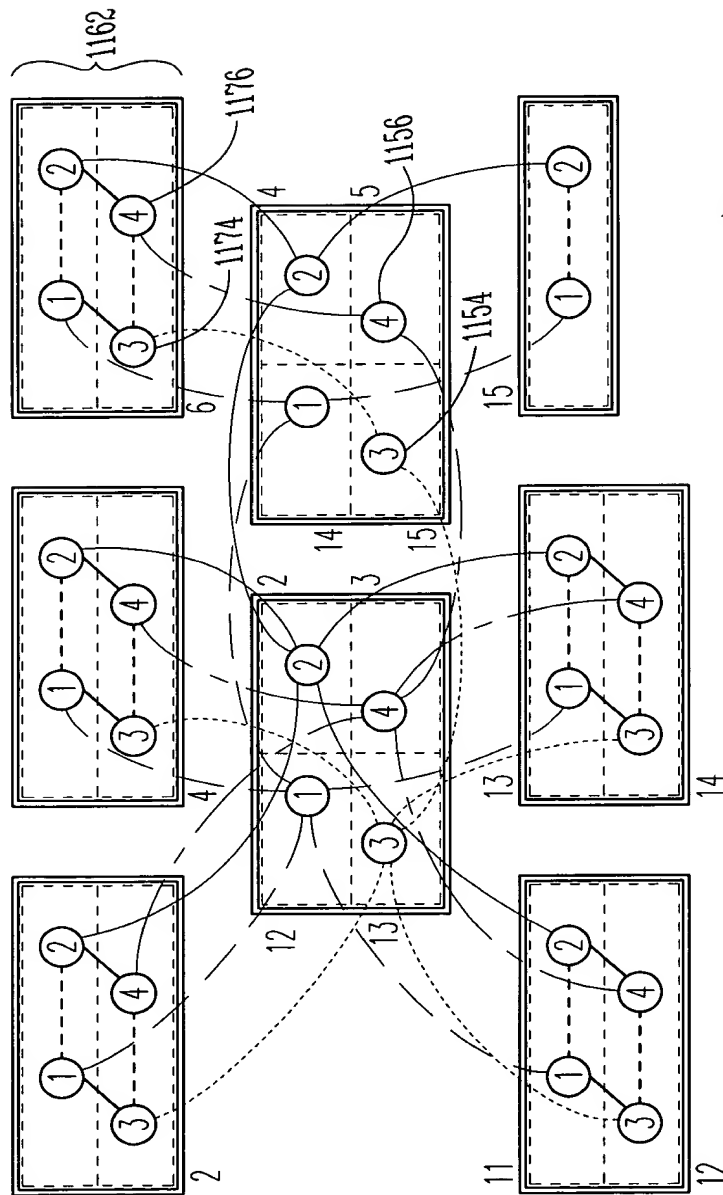
Fig. 10





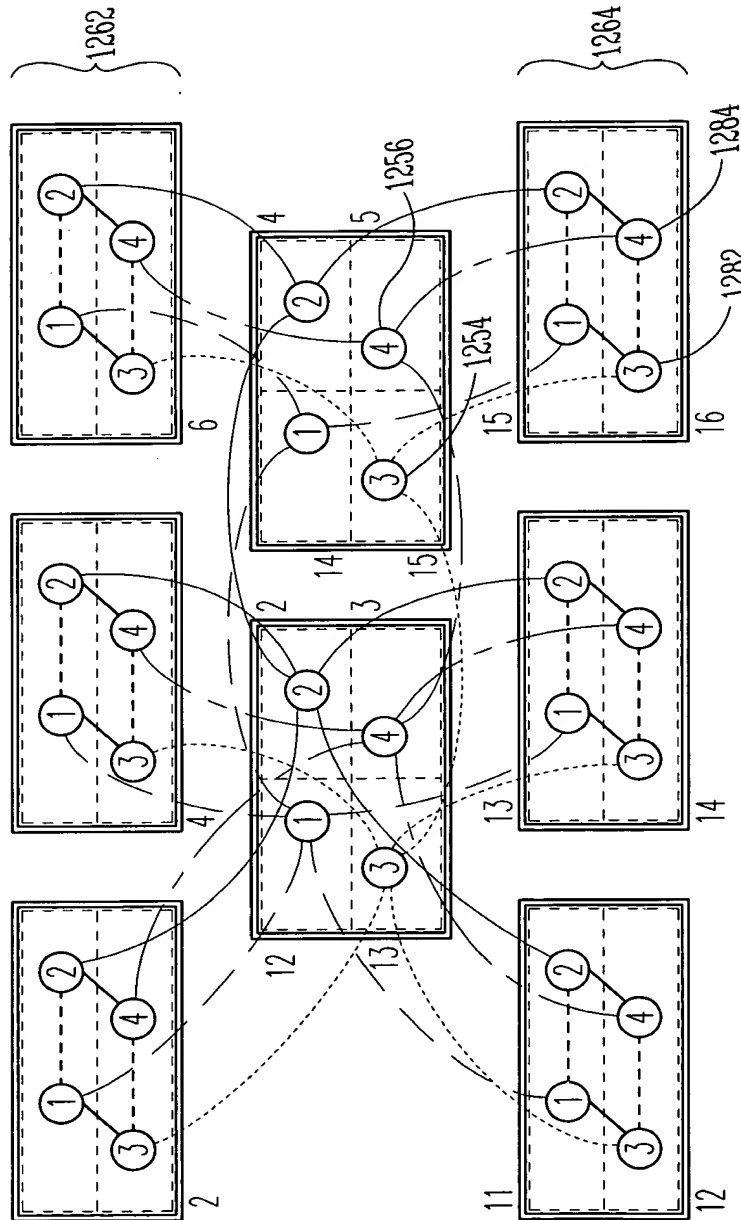
TITLE: NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM  
INVENTORS NAME: Martin M. Deneroff et al.  
SERIAL NO.: 09/408,972

9/15



RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 11



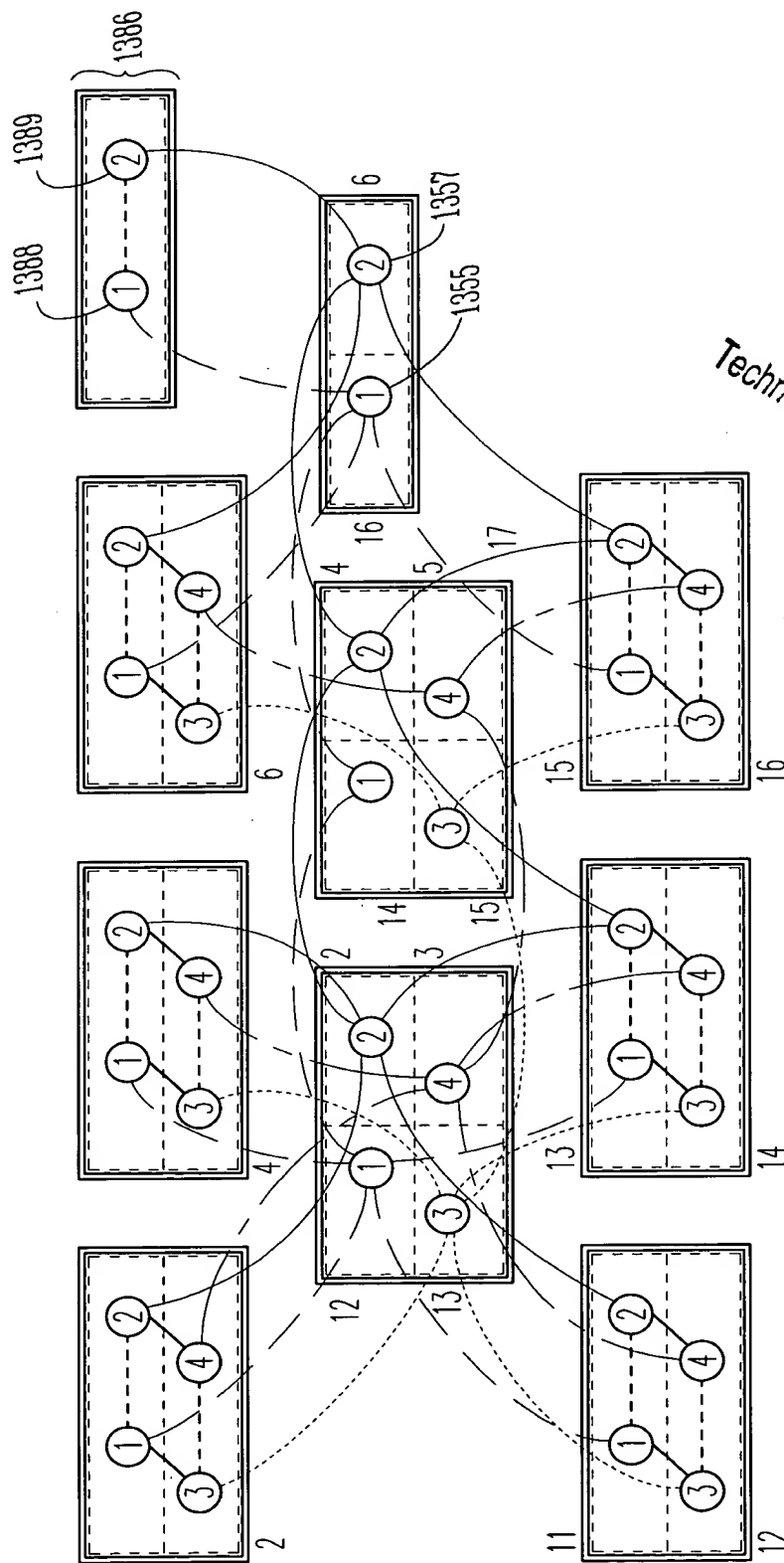
RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 12



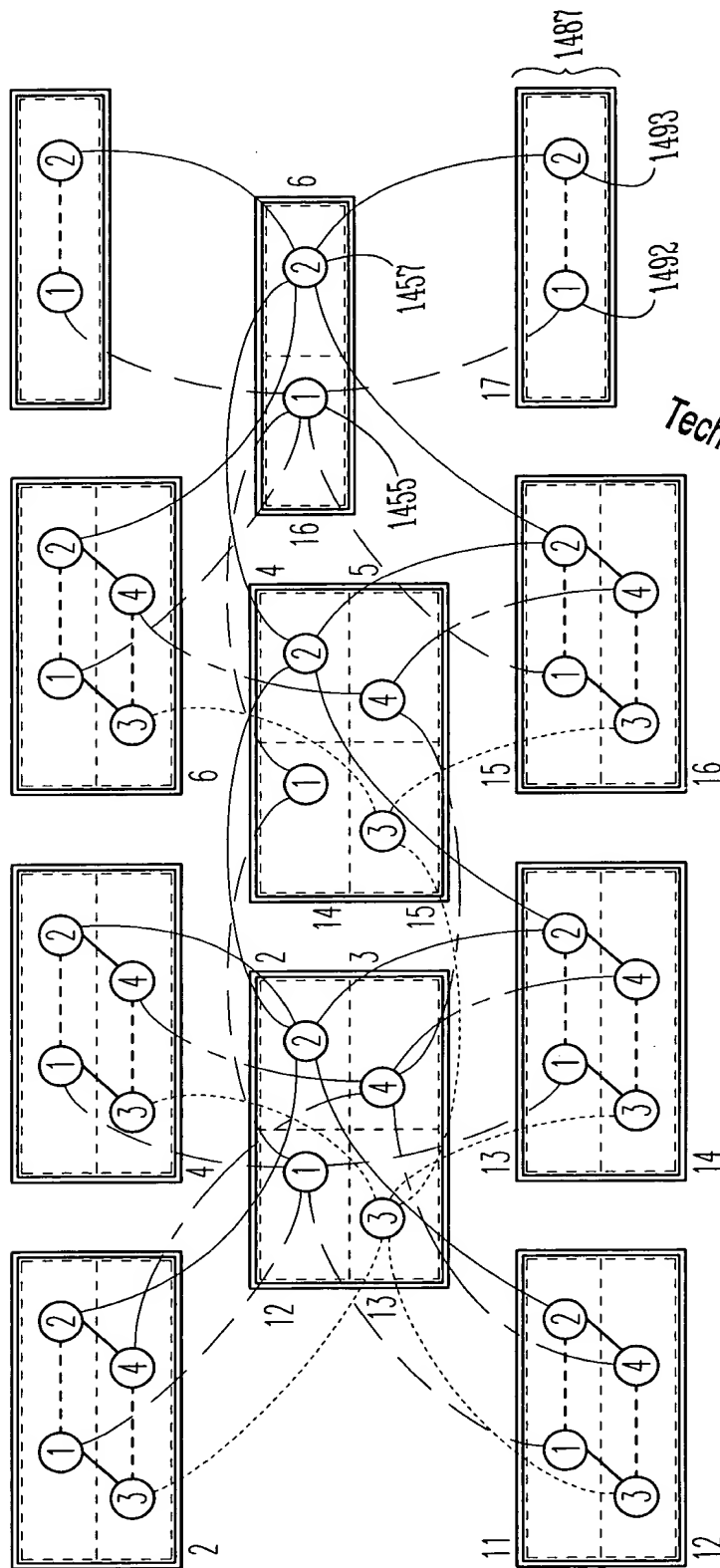
TITLE: NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM  
INVENTORS NAME: Martin M. Deneroff et al.  
SERIAL NO.: 09/408,972

11/15



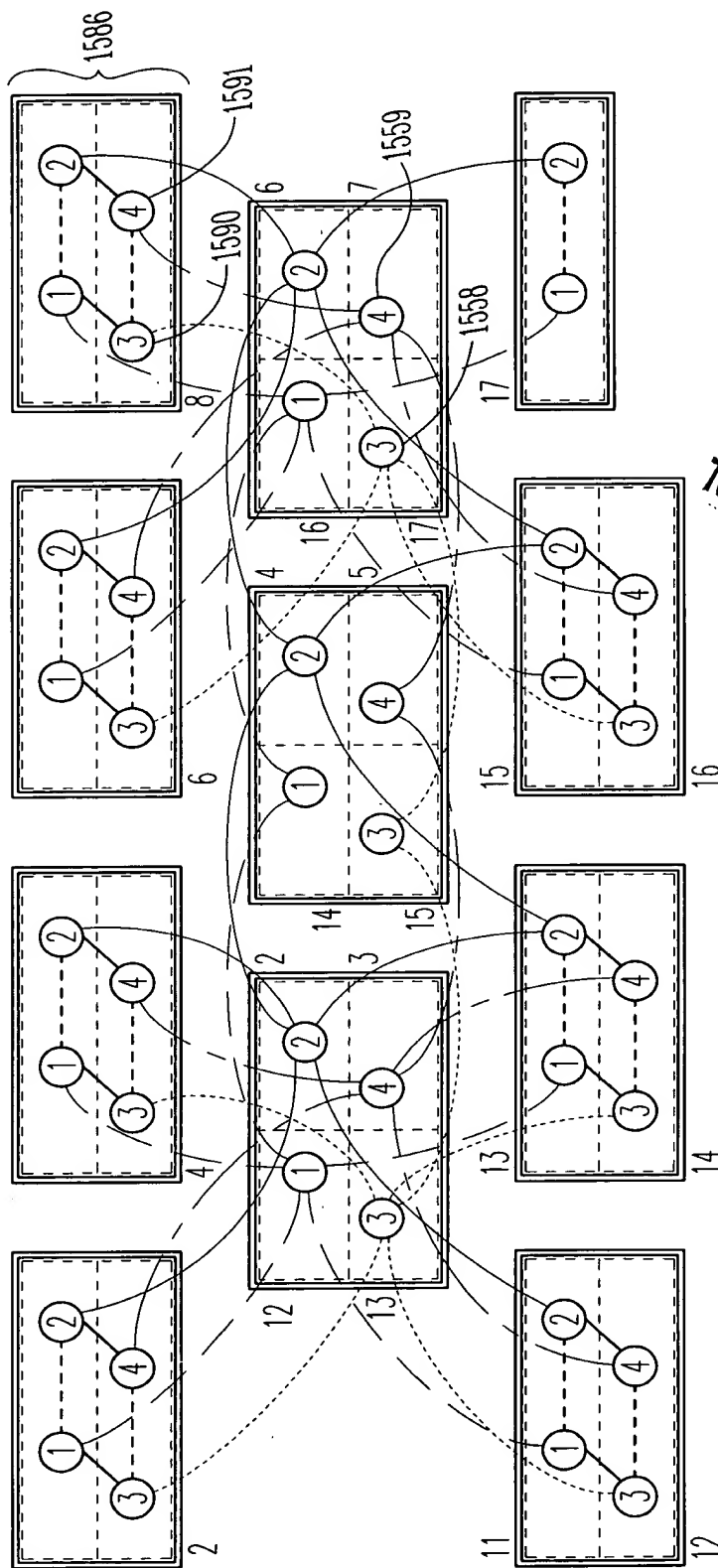
RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 13



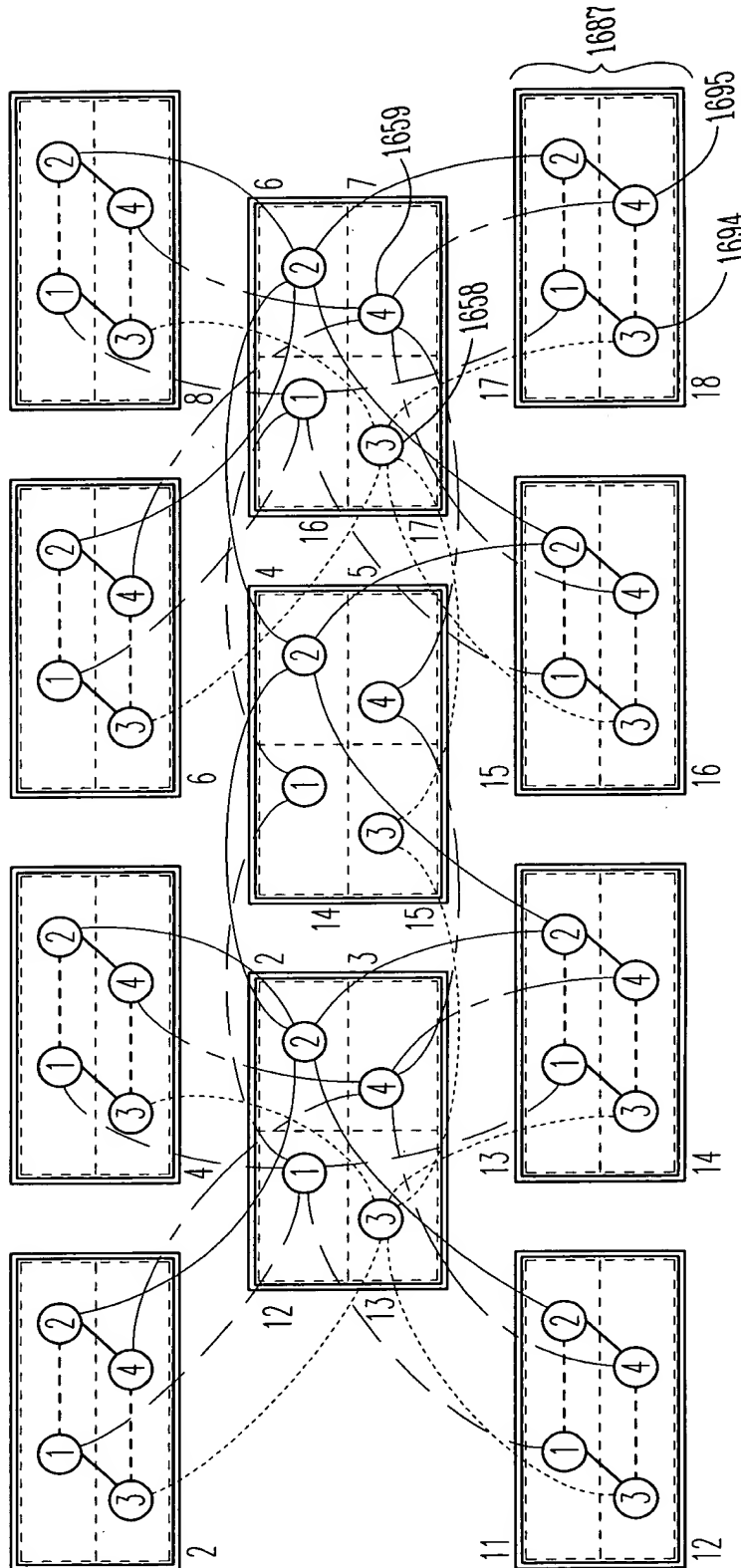
RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 14



RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 15



RECEIVED  
SEP 09 2002  
Technology Center 2100

Fig. 16



TITLE: NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM  
INVENTORS NAME: Martin M. Deneroff et al.  
SERIAL NO.: 09/408,972

15/15

RECEIVED  
SEP 09 2002  
Technology Center 2100  
Fig. 17

